SEP 0 2 2003 E

INFORMATION OF THE SUBJECT APPLICATION

Applicant:	TY. Chang et al.	Attorney Docket N	No. SIPT121256	
Application No.:	10/601,709	Group Art Unit: -		
Filed:	June 19, 2003	Examiner:	•	÷ .
Title:	INTEGRATED CIRCUIT DE CIRCUIT FOR DETECTING EMBEDDED MEMORY			
Acceptance	U.S. PATENT I	DOCUMENTS		
*Examiner Cite Initials No.	Document No. / Code	Date (mm/dd/yyyy) N	ame .	
<u>4</u> 0 U1	6,424,583	07/23/2002 Si	ung et al.	
FOREIGN PATENT DOCUMENTS				
*Examiner Cite Initial No.		cation Date n/dd/yyyy)	Country	English Abstract Translati
None.	OTHER INFO (Including Author, Title, D		s, Etc.)	
*Examiner Cité Initial No.				
<u>CD</u> 01	Sung and Wu, A Method of En IEEE, 462-465 (2001)	nbedded Memory A	ccess Time Meas	urement,
<u>(4)</u> O2	Lee, Hsiao and Chang, An Acc Memory, IEEE, 104-109 (2002)	cess Timing Measur 2)	ement Unit of En	nbedded
Examiner	Date	Considered		

*Examiner: Initial if reference considered, whether or not citation is in conformance with M.P.E.P. § 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. SIL:DDP

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